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CONFIRMATION NO. 7488

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APPLICANTS

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** CONTINUING DATA ***** *TN*

This application is a DIV of 09/764,432 01/19/2001 PAT 6,664,144
which is a DIV of 09/114,223 07/13/1998 PAT 6,242,290

** FOREIGN APPLICATIONS ***** *TN*

JAPAN 9-205345 07/14/1997

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>TN</i> Initials	JAPAN	6	51	7

ADDRESS

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TITLE

Method of fabricating semiconductor device

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of
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